

What is claimed is:

1. A semiconductor device including a rewiring, mixedly  
comprising:

an input/output (I/O) cell connected to a probing pad; and  
5 another input/output (I/O) cell having no probing pad.

2. A semiconductor device according to claim 1, further  
comprising a DRAM.

10 3. A semiconductor device according to claim 1, further  
comprising a fuse element.

4. A semiconductor device according to claim 1, wherein the  
semiconductor device lacks a probing pad on at least one side.

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